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**1 Synthesis of current mode building blocks for fuzzy logic control cir**  
*Patyra, M.J.; Long, J.E.;*

Circuits and Systems, 1994. ISCAS '94., 1994 IEEE International Symposium on , Volume: 4 , 30 May-2 June 1994  
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**2 CMOS fuzzy logic controller in current mode**
*Lemaitre, L.; Patyra, M.J.; Mlynek, D.;*

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**3 Programmable fuzzy logic controller circuit on CPLD chip**
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**4 A fuzzy membership function circuit using hysteretic resonant tunn diodes**
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**7 A fault model and a test method for analog fuzzy logic circuits**

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**1 Implementation of O(n) complexity max/min circuits for fuzzy and connectionist computing**
*Hassoun, M.H.; Nabha, A.M.;*

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**3 Design and preliminary results of high speed analog 1.0 μm CMOS MAX circuit for fuzzy architectures**
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**15 Recursive training for multi-resolution fuzzy min-max neural network classifier**

*Chen Xi; Jin Dongming; Li Zhijian;*

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Pages:131 - 134 vol.1

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